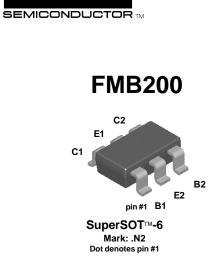
FMB200



FAIRCHILD

PNP Multi-Chip General Purpose Amplifier

This device is designed for general purpose amplifier applications at collector currents to 300 mA. Sourced from Process 68.

Absolute Maximum Ratings* T_A = 25°C unless otherwise noted

Symbol	Parameter	Value	Units
V _{CEO}	Collector-Emitter Voltage	45	V
V _{CBO}	Collector-Base Voltage	60	V
V _{EBO}	Emitter-Base Voltage	6.0	V
Ic	Collector Current - Continuous	500	mA
T _J , T _{stg}	Operating and Storage Junction Temperature Range	-55 to +150	°C

*These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

NOTES:

 1) These ratings are based on a maximum junction temperature of 150 degrees C.
2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations. 3) All voltages (V) and currents (A) are negative polarity for PNP transistors.

Thermal Characteristics T_A = 25°C unless otherwise noted

Symbol	Characteristic	Мах	Units
		FMB200	
PD	Total Device Dissipation	700	mW
	Derate above 25°C	5.6	mW/°C
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	180	°C/W

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PNP Multi-Chip General Purpose Amplifier

(continued)

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Electrical Characteristics	$T_A = 25^{\circ}C$ unless otherwise noted

SymbolParameterTest ConditionsMinTypMaxUnits
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OFF CHARACTERISTICS

ВV _{CBO}	Collector-Base Breakdown Voltage	$I_{C} = 10 \ \mu A, \ I_{B} = 0$	60		V
BV_{CEO}	Collector-Emitter Breakdown Voltage*	$I_{\rm C} = 1.0 \text{ mA}, I_{\rm E} = 0$	45		V
BV_{EBO}	Emitter-Base Breakdown Voltage	$I_E = 10 \ \mu A, \ I_C = 0$	6.0		V
I _{CBO}	Collector Cutoff Current	$V_{CB} = 50 \text{ V}, I_E = 0$		50	nA
I _{CES}	Collector Cutoff Current	$V_{CE} = 40 \text{ V}, I_E = 10$		50	nA
I _{EBO}	Emitter Cutoff Current	$V_{EB} = 4.0 \text{ V}, I_{C} = 0$		50	nA

ON CHARACTERISTICS

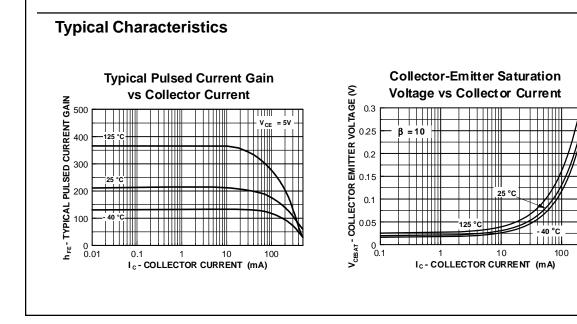
h _{FE}	DC Current Gain	$I_{C} = 100 \ \mu A, V_{CE} = 1.0 \ V$	80		
		$I_{C} = 10 \text{ mA}, V_{CE} = 1.0 \text{ V}$	100	450	
		I _C = 150 mA, V _{CE} = 5.0 V*	100	350	
V _{CE(sat)}	Collector-Emitter Saturation Voltage	$I_{\rm C} = 10 \text{ mA}, I_{\rm B} = 1.0 \text{ mA}$		0.2	V
- (,		$I_{C} = 200 \text{ mA}, I_{B} = 20 \text{ mA}^{*}$		0.4	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	$I_{\rm C} = 10 \text{ mA}, I_{\rm B} = 1.0 \text{ mA}$		0.85	V
(```	_	$I_{C} = 200 \text{ mA}, I_{B} = 20 \text{ mA}^{*}$		1.0	V

SMALL SIGNAL CHARACTERISTICS

f⊤	Current Gain - Bandwidth Product	V _{CE} = 20 V, I _C = 20 mA	300	MHz
Cobo	Output Capacitance	$V_{CB} = 10 \text{ V}, \text{ f} = 1.0 \text{ MHz}$	4.5	pF
NF	Noise Figure	I_{C} = 100 μA, V _{CE} = 5.0 V, R _G = 2.0 kΩ, f = 1.0 kHz	2.5	dB

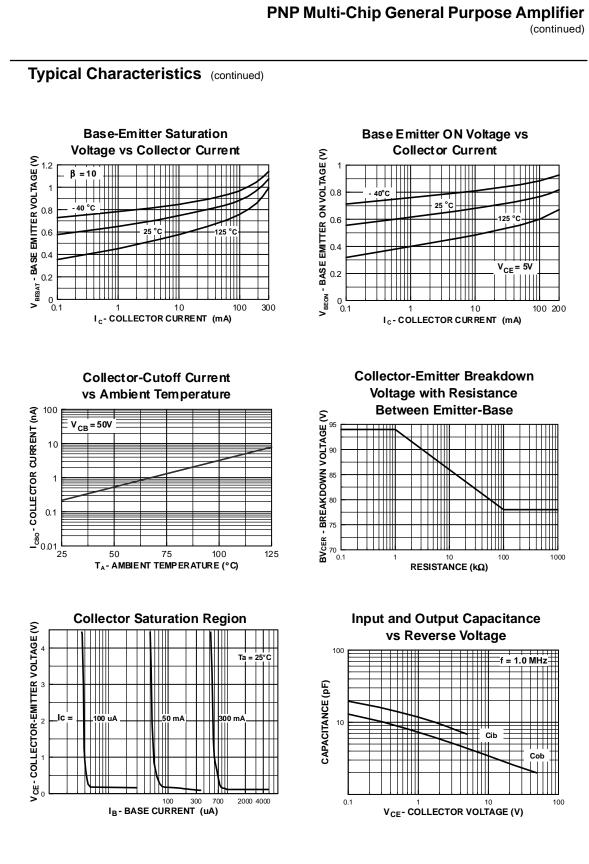
*Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2.0%

NOTE: All voltages (V) and currents (A) are negative polarity for PNP transistors.

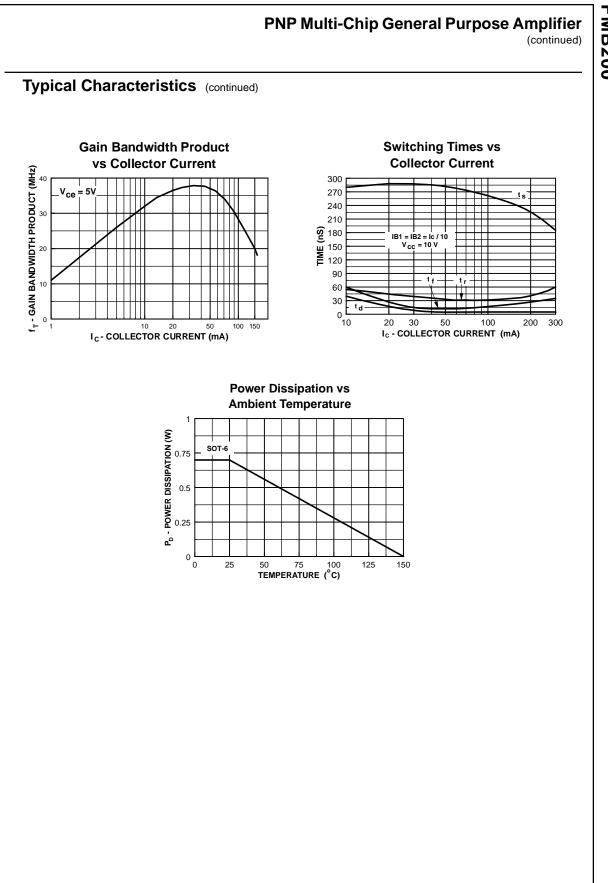


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Definition of Terms

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No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
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